

The diagram illustrates a redundancy control system 10. It includes an external input 16 labeled "ERASURE COM." and an external input 17 labeled "ADDRESS". The system contains a "CONTROLLER" 11, a "REGISTER" 13, a "SPECIAL STORAGE REGION" 15, and a "REDUNDANCY JUDGING CIRCUIT" 14. The "CONTROLLER" 11 is connected to the "REGISTER" 13, the "SPECIAL STORAGE REGION" 15, and a set of storage units labeled "NORMAL" 12a, "DEFICIENT" 12b, and "REDUNDANT" 12c. The "REGISTER" 13 outputs to the "REDUNDANCY JUDGING CIRCUIT" 14. The "SPECIAL STORAGE REGION" 15 outputs to the "REDUNDANCY JUDGING CIRCUIT" 14 via a signal line labeled "A". The "REDUNDANCY JUDGING CIRCUIT" 14 outputs to the "CONTROLLER" 11. The "DEFICIENT" 12b and "REDUNDANT" 12c units output to the "REDUNDANCY JUDGING CIRCUIT" 14. The "REDUNDANCY JUDGING CIRCUIT" 14 also outputs to the "CONTROLLER" 11. The "CONTROLLER" 11 outputs to the "ERASURE COM." 16 and the "ADDRESS" 17. The "CONTROLLER" 11 also outputs to the "NORMAL" 12a, "DEFICIENT" 12b, and "REDUNDANT" 12c units. The "REDUNDANT" 12c unit outputs to the "ERASURE STATUS" and "DEFICIENT ERASURE STATUS" signals.

Fig. 3

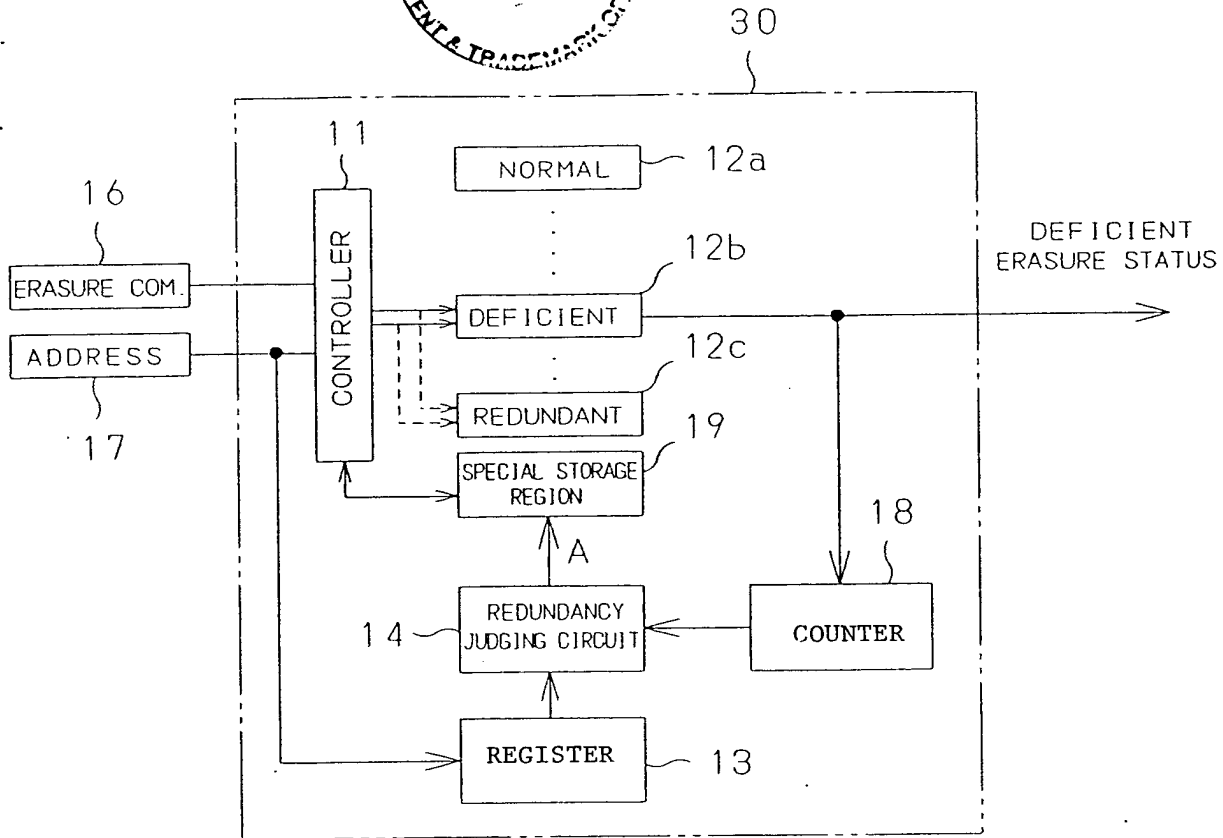
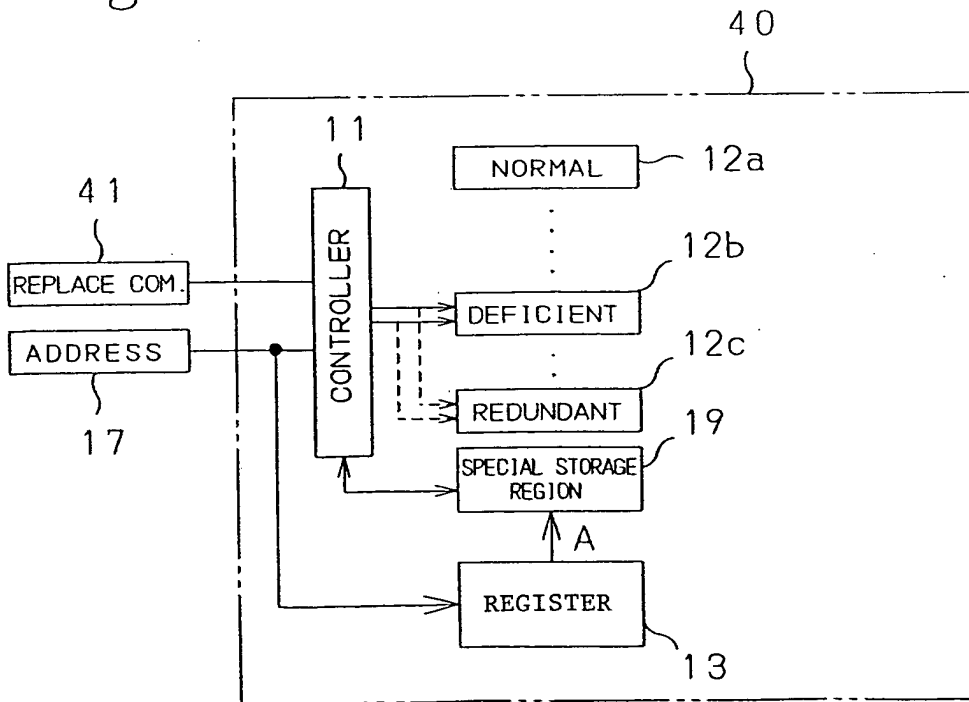


Fig. 4



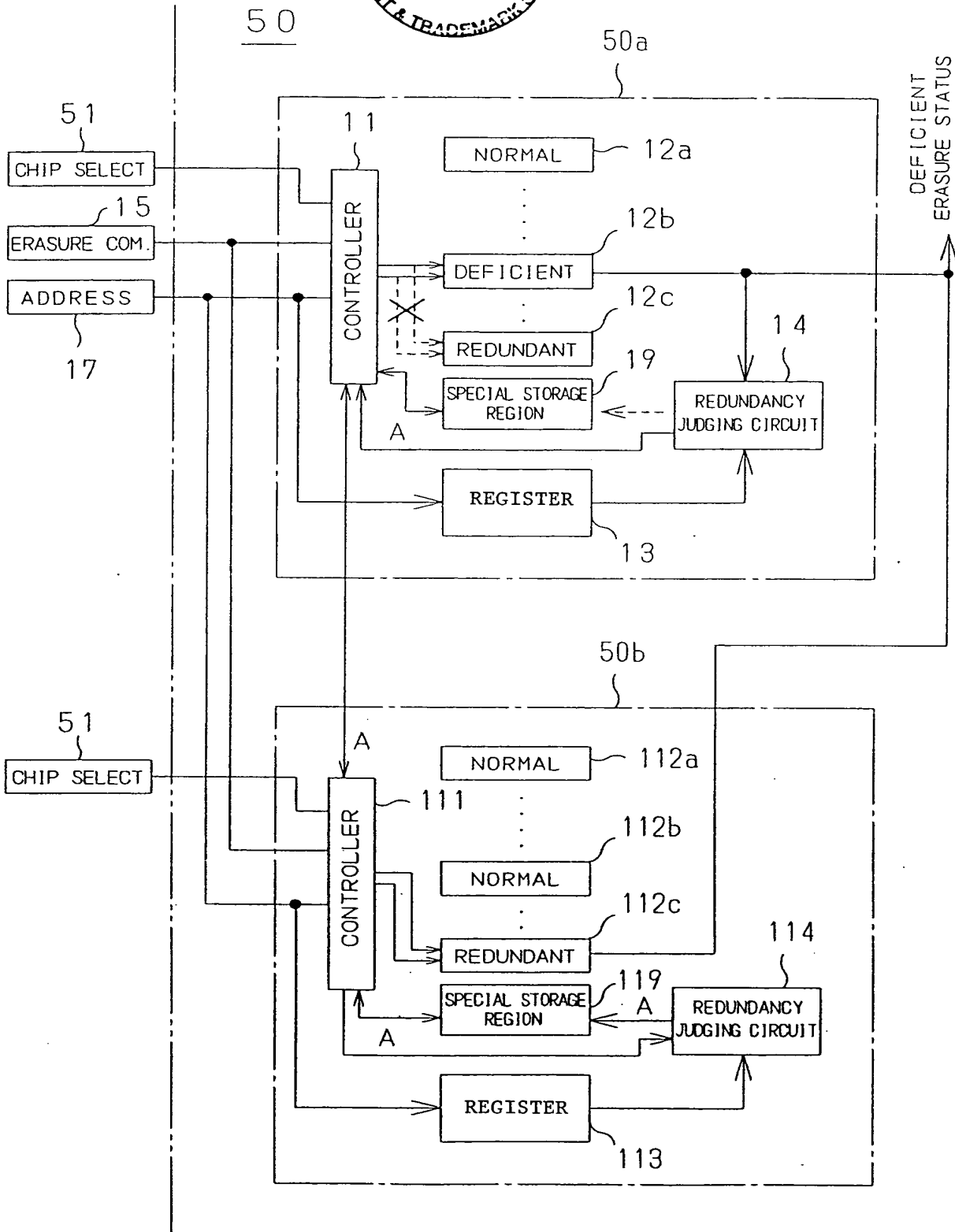


Fig. 6

